

08/15/01

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

## U.S. UTILITY Patent Application

<p><b>O.I.P.E.</b></p> <p>SCANNED <i>SD</i> Q.A. <i>VR</i></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO. 09/882538	CONT/PRIOR	CLASS 257	SUBCLASS 197	ART UNIT <del>2811</del> 5814	EXAMINER <del>G. CHAIKIN</del> E. ADHILL
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## APPLICANTS

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**TITLE**

Method for forming a super self-aligned hetero-junction bipolar transistor

PTO-2040  
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____			
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
	_____ (Legal Instruments Examiner) (Date)		Amount Due	Date Paid
			<b>ISSUE BATCH NUMBER</b>	
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